10/074792

PTO/SB/21 (09-04) (AW 10/2004)
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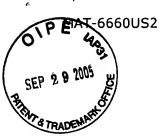
Total Number of Pages in This Submission 7

Patent Number	6,861,744	
Issue Date	March 1, 2005	
First Named Inventor	Masaaki Hayama, et al.	
Art Unit	2814	
Examiner Name	Alonzo Chambliss	
Attorney Docket No.	MAT-6660US2	

ENCLOSURES (Check all that apply)		
Fee Transmittal Form Fee Attached	Drawing(s) After Allowance Communication to TC Licensing-related Papers	
Amendment/Reply After Final Affidavits/Declaration(s) Extension of Time Request Express Abandonment Request Information Disclosure Statement Certified Copy of Priority Document(s) Response to Missing Parts/ Incomplete Application Response to Missing Parts	☐ Petition Appeal Communication to Board of Appeals and Interferences ☐ Petition to Convert to a Provisional Application Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) ☐ Power of Attorney, Revocation, Change of Correspondence Address ☐ Proprietary Information ☐ Terminal Disclaimer ☐ Other Enclosure(s) (please identify below): Request for Certificate of Correction; PTO-1050; Copy of Page 2 of 05/12/2003 Amendment; Copy of Page 3 of 08/05/2004 Amendment; Return Receipt Postcard	
under 37 CFR 1.52 or 1.53	Remarks:	
SIGNATURE OF APPLICANT, ATTORNEY OR AGENT Certificate		
Firm Name RatherPrestia SEP 3 0 2005 Signature Printed Name Lawrence E. Ashery Of Correction Date September 27, 2005 Registration No. 34,515		
CERTIFICATE OF TRANSMISSION / MAILING		
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Number: 6, Issued: M

6,861,744 March 1, 2005

Name of Patentee:

Masaaki Hayama, et al.

Title of Invention:

A MULTILAYER CERAMIC SUBSTRATE UTILIZING AN INTAGLIO PLATE

WITH A PLURALITY OF GROOVES HAVING DIFFERENT DEPTHS

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR PTO MISTAKE (37 C.F.R. § 1.322(a))

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Attention:

Certificate of Correction Branch

- 1. Attached in duplicate is Form PTO-1050 with at least one copy being suitable for printing.
- 2. Correction of the Official Letters Patent is respectfully requested in view of the following text which appears correctly in the application file:

On the cover page at the title of invention (54), before "MULTILAYER" insert -- A --, as indicated on page 2 of the amendment filed May 12, 2003.

At Column 12, line 34, "at" should read -- of --, as indicated on page 3 of the amendment filed on August 5, 2004.

3. Please send the Certificate to:

Name:

Lawrence E. Ashery

P.O. Box 980

Valley Forge, PA 19482

(610) 407-0700

MAT-6660US2 PATENT

Name of Assignee: Matsushita Electric Industrial Co., Ltd.

Assignment Recorded on: February 1, 1999

Reel:

9734

Frame:

0737

/Respectfully submitted,

Lawrence E. Ashery, Reg. 34,515 Attorney for Applicants

LEA/dmw

Enclosures: Form PTO

Form PTO-1050 (in duplicate)

Copy of Page 2 of 05/12/2003 Amendment Copy of Page 3 of 08/05/2004 Amendment

Dated: September 27, 2005

P.O. Box 980 Valley Forge, PA 19482 (610) 407-0700

The Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. 18-0350 of any fees associated with this communication.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 Attn: Certificate of Correction Branch on September 27, 2005.

DMW_I:\MAT\6660US2\CERTCORRECTION.DOC

- 2 -

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO:

6,861,744

Page 1 of 1

DATED:

MARCH 1, 2005

APPLICATION NO.:

10/074,792

INVENTOR(S):

MASAAKI HAYAMA, ET AL.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Cover Page

At title of invention (54), before "MULTILAYER" insert -- A --.

Column 12

Line 34, "at" should read -- of --.

Mailing Address of Sender:

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This collection of information is required by 37 CFR 1.322, 1.323 and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance completing the form, call 1-800-PTO-9199 and select option 2.

Application No.:
Amendment dated:

10 Ma

Reply to Office Action of:

10/074,792 May 12, 2003 January 15, 2003 MAT-6660US2

Amendments to the Specification:

IN THE TITLE:

Please replace the Title with the following:

<u>A</u> MULTILAYER CERAMIC SUBSTRATE UTILIZING AN INTAGLIO PLATE WITH A PLURALITY OF GROOVES HAVING DIFFERENT DEPTHS

Application No.: Amendment Dated: Reply to Final Office Action of: May 12, 2004

10/074,792 August 5, 2004 MAT-6660US2

- 19. (Previously Presented) The multilayer ceramic substrate of claim 13, further comprising an LSI chip mounted on a part of one of said first and second conductive patterns with the face down and electrically connected.
- 20. (Previously Presented) The multilayer ceramic substrate of claim 13, further comprising an LSI chip mounted on a part of one of said first and second conductive patterns with the face down and electrically connected through an electroconductive paste applied on the top of a fine bump provided on one of said first and second conductive patterns, said fine bump formed by using a second groove which is disposed on said intaglio at a place corresponding to a pad of said LSI chip.
- 21. (Previously Presented) The multilayer ceramic substrate of claim 13, further comprising an LSI package mounted on a part of one of said first and second conductive patterns with the face down and electrically connected through a lattice of lands with a pitch of not larger than 0.8mm, said lattice provided on one of said first and second conductive patterns.

22-28. (Cancelled).

- 29. (Previously Presented) The multilayer ceramic substrate of claim 13, wherein said first conductive pattern being a transfer of an intaglio plate.
- 30. (Cancelled).
- 31. (New) The multilayer ceramic substrate of claim 13, wherein said via extends above a top surface of said insulation layer.